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NEW QUESTION: 1The outputs from risk identification are usually contained in the document called a risk register. All of the following should be included in the risk register upon completion of the risk identification process EXCEPT:**A.** List of potential responses**B.** Risk audits**C.** Root causes of risks**D.** List of identified risks**E.** Update risk categories**Answer: B**

NEW QUESTION: 2

デバイスの起動中にカーネルにパッチを適用して、連続して再起動するたびにカーネルがジェイルブレイクされるようにするiosジェイルブレイク手法はどれですか？**A.** セミテザー脱獄**B.** テザー脱獄**C.** セミアンテガードジェイルブレイキング**D.** 拘束されていない脱獄**Answer: A**

Explanation:ExplanationA semi-tethered jailbreak is one that allows a handset to finish a boot cycle when being pwned, however jailbreak extensions won't load till a laptop-based jailbreak application is deployed over a physical cable association between the device and also the computer in question.Semi-tethered jailbreaks aren't as difficult as tethered jailbreaks as a result of you'll be able to power cycle your device and expect to use it commonly thenceforth, like creating phone calls and causing text messages.On the opposite hand, jailbreak tweaks won't initialize on the freshly-booted device and jailbreak-based apps like Cydia and Filza can merely crash on launch them till the device is shod back to a jailbroken state.Just as the name implies, a semi-'tethered' jailbreak necessitates a physical cable association between the device and also the laptop once running the jailbreak tool to patch the kernel and reinitialize the jailbroken state, however the nice issue here is that you simply will still access important core smartphone practicality in an exceedingly pinch after you don't have a laptop near .The spic-and-span checkra1n jailbreak tool for macOS (and before long Windows) could be a prime example of a semi-tethered jailbreak, and may pwn A7-A11-equipped devices as previous because the iPhone 5s and as new because the iPhone X.

NEW QUESTION: 3You have a server named Server1 that runs Windows Server 2012 R2.You discover that the performance of Server1 is poor.The results of a performance report generated on Server1 are shown in the following table.You need to identify the cause of the performance issue.What should you identify?**A.** NUMA fragmentation**B.** Insufficient RAM**C.** Excessive paging**D.** Driver malfunction**Answer: D**Explanation:Processor: %DPC Time. Much like the other values, this counter shows the amount of time that the processor spends servicing DPC requests. DPC requests are more often than not associated with the network interface.Processor : % Interrupt Time. This is the percentage of time that the processor is spending on handling Interrupts. Generally, if this value exceeds 50% of the processor time you may have a hardware issue. Some components on the computer can force this issue and not really be a problem. For

example a programmable I/O card like an old disk controller card, can take up to 40% of the CPU time. A NIC on a busy IIS server can likewise generate a large percentage of processor activity. Processor : % User Time. The value of this counter helps to determine the kind of processing that is affecting the system. Of course the resulting value is the total amount of non-idle time that was spent on User mode operations. This generally means application code. Processor : %Privilege Time. This is the amount of time the processor was busy with Kernel mode operations. If the processor is very busy and this mode is high, it is usually an indication of some type of NT service having difficulty, although user mode programs can make calls to the Kernel mode NT components to occasionally cause this type of performance issue. Memory : Pages/sec. This value is often confused with Page Faults/sec. The Pages/sec counter is a combination of Pages Input/sec and Pages Output/sec counters. Recall that Page Faults/sec is a combination of hard page faults and soft page faults. This counter, however, is a general indicator of how often the system is using the hard drive to store or retrieve memory associated data.<http://technet.microsoft.com/en-us/library/cc768048.aspx>

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